

Notice of References Cited	Application/Control No. 10/010,238	Applicant(s)/Patent Under Reexamination BLATT ET AL.	
	Examiner Thomas H. Stevens	Art Unit 2121	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,555,201	09-1996	Dangelo et al.	716/1
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Mudge, T., "Power: A First-Class Architectural Design Constraint" INSPEC pg.-52-58.
	V	Musoll et al., "Scheduling and Resource Binding for Low Power" 1995 8th International Symposium on System Synthesis" pg.104-109.
	W	Seng et al., "Power-Sensitive Multithreaded Architecture" Sept. 2000, pg. 17-20.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
 Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.